WEST

Freeform Search

Database:
- US Patents Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Term:
```
15 not 16
```

Display: `30` Documents in **Display Format:** `CIT` **Starting with Number** `1`

Generate: ○ **Hit List** ● **Hit Count** ○ **Image**

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Search History

Today's Date: 6/20/2000

DB Name	Query	Hit Count	Set Name
USPT	l5 not l6	11	L8
USPT	l4 not l6	12	L7
USPT	l4 and l5	8	L6
USPT	l1 same l3	19	L5
USPT	l2 same l3	20	L4
USPT	graphics adj2 (processor or engine or accelerat$)	2668	L3
USPT	vector adj2 processor or vector adj2 processing	2343	L2
USPT	vector adj2 operation	1459	L1

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☐   Generate Collection

L8: Entry 4 of 11   File: USPT   Aug 4, 1998

DOCUMENT-IDENTIFIER: US 5790854 A
TITLE: Efficient stack utilization for compiling and executing nested if-else
constructs in a vector data processing system

BSPR:
Data processing systems which execute <u>vector operations</u> are becoming increasingly
popular in many data intensive application including those in the areas of fuzzy
logic, neural network, and <u>graphics accelerator</u> applications due to their
considerable performance and cost benefits. Additionally, with the increased
execution of <u>vector operations,</u> the corresponding difficulty of programming data
processors to execute operations in parallel has proportionally increased. In
particular, compilers required to translate a programmers language into code
comprehensible by the data processor have encountered a number of obstacles.

Reason

5,600,811

Mark Larson

954 - 925,
1100
08/969,702
1-9 (allow)